

# EXHIBIT 6

[PUBLIC VERSION]

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>	Complete if Known	
	<b>Application Number</b>	12/464,572
	<b>Filing Date</b>	05/12/2009
	<b>First Named Inventor</b>	Rudy G.H. Eschauzier
	<b>Group Art Unit</b>	
	<b>Examiner Name</b>	
Sheet 1 of 1		Attorney Docket No: 55123P376

US PATENT DOCUMENTS					
Examiner Initial *	Cite No <sup>1</sup>	USP Document Number	Publication or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No <sup>1</sup>	Foreign Patent Document Country Code/Number/Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>2</sup>

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		DESSOUKY, MOHAMED , et al., "Very Low-Voltage Digital-Audio $\Delta\Sigma$ Modulator with 88-dB Dynamic Range Using Local Switch Bootstrapping", <u>IEEE Journal of Solid-State Circuits</u> , Vol. 36, No. 3, (March 2001), Pgs. 349-355	
		ENZ, CHRISTIAN C., et al., "Circuit Techniques for Reducing the Effects of Op-Amp Imperfections: Autozeroing, Correlated Double Sampling, and Chopper Stabilization", <u>Proceedings of the IEEE</u> , Vol. 84, No. 11, (November 1996), Pgs. 1584-1614	
		ESCHAUZIER, RUDY G., et al., "Frequency Compensation Techniques for Low-Power Operational Amplifiers", Section 6.1, Springer, (1995)	
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		HUIJSING, JOHAN H., "Operational Amplifiers, Theory and Design", Kluwer Academic Publishers, (2001), Pgs. 52-53	
		WESTE, NEIL H., et al., "Principles of CMOS VLSI Design, A Systems Perspective, Second Edition", Section 5.5.10, Addison-Wesley Publishing Company, (1993), Pgs. 346-350	

EXAMINER

DATE CONSIDERED